

SECTION SEVEN

9M71B
2/1/63
PAGE 979

DST SHOULD NOT SWITCH TO I UNTIL THE SECOND E CYCLE

71026	0560	00	0	74210	OL3	LDQ	ONES	LOAD MQ
71027	-0754	00	0	00000		PXD	0,0	
71030	-0603	00	0	74225		DST	TEMP	
71031	0500	00	0	74226		CLA	TEMP+1	
71032	-0100	00	0	71035		TNZ	++3	
71033	0760	00	0	00162		SWT	2	
71034	0420	00	0	00000		HPR		ERROR DST- CLA
71035	0760	00	0	00161		SWT	1	
71036	0020	00	0	71040		TRA	++2	
71037	0020	00	0	71026		TRA	OL3	

CHECK THAT INDIRECT CYCLE IS NOT SWITCHED TO I TIME

71040	-0754	00	0	00000	OL4	PXD	0,0	
71041	0560	00	0	74210		LDQ	ONES	L CONSTANT INTO MQ
71042	0774	00	1	00000		AXT	0,1	
71043	0600	00	0	74225		STZ	TEMP	
71044	-0600	60	0	71030		STQ*	OL3+2	
71045	0500	00	0	74225		CLA	TEMP	
71046	-0100	00	0	71051		TNZ	++3	
71047	0760	00	0	00162		SWT	2	
71050	0420	00	0	00000		HPR		ERROR STQ*-CLA
71051	0760	00	0	00161		SWT	1	
71052	0020	00	0	71054		TRA	++2	
71053	0020	00	0	71040		TRA	OL4	

TEST SCA WITH SUB IN ODD LOCATION

71054	0774	00	1	00004	OL5	AXT	4,1	
71055	0756	00	1	00000		PCA	0,1	
71056	0636	00	1	74225		SCA	TEMP,1	
71057	0402	00	0	74225		SUB	TEMP	
71060	-0100	00	0	71063		TNZ	++3	
71061	0760	00	0	00162		SWT	2	
71062	0420	00	0	00000		HPR		ERROR SCA - SUB
71063	0760	00	0	00161		SWT	1	
71064	0020	00	0	71066		TRA	++2	
71065	0020	00	0	71054		TRA	OL5	

SECTION SEVEN

9M718
2/1/63
PAGE 981

TEST FOR FALSE DIVIDE CHECK ON STO-DVP

71143	0760	00	0	00012	OL6AA	DCT		RESET DIVIDE
71144	0761	00	0	00000		NOP		CHECK TRIGGER
71145	0761	00	0	00000		NOP		
71146	0560	00	0	74200		LDQ	ONE	
71147	0500	00	0	74224		CLA	ZERO	
71150	0601	00	0	74225		STO	TEMP	***
71151	0221	00	0	74204		DVP	NINE	***
71152	0761	00	0	00000		NOP		
71153	0760	00	0	00012		DCT		
71154	0020	00	0	71156		TRA	++2	
71155	0020	00	0	71160		TRA	++3	
71156	0760	00	0	00162		SWT	2	
71157	0420	00	0	00000		HPR		
71160	0760	00	0	00161		SWT	1	
71161	0020	00	0	71163		TRA	++2	
71162	0020	00	0	71143		TRA	OL6AA	

TEST THAT TXL IN ODD LOCATION IS NOT DECODED AS STR

71163	0441	00	0	00002	OL6B	LDI	2	SAVE CONTENTS
71164	0500	00	0	71172		CLA	OL6B1	OF LOCATION TWO
71165	0601	00	0	00002		STO	2	
71166	0774	00	1	00400		AXT	256,1	
71167	0761	00	0	00000		NOP		
71170	0500	00	0	71163		CLA	OL6B	***
71171	-3 00401	1		71175		TXL	++4,1,257	***
71172	0021	00	0	71173	OL6B1	TTR	++1	* CONSTANT *
71173	0760	00	0	00162		SWT	2	
71174	0420	00	0	00000		HPR		INDICATE ERROR
71175	0760	00	0	00161		SWT	1	
71176	0020	00	0	71200		TRA	++2	PROCEED
71177	0020	00	0	71163		TRA	OL6B	REPEAT THIS TEST

TEST THAT STQ IN ODD LOCATION IS NOT DECODED AS STR

71200	0500	00	0	71205	OL6C	CLA	OL6C1	SET UP
71201	0601	00	0	00002		STO	2	LOCATION TWO
71202	0560	00	0	74225		LDQ	TEMP	***
71203	-0600	00	0	74225		STQ	TEMP	***
71204	0020	00	0	71210		TRA	++4	
71205	0021	00	0	71206	OL6C1	TTR	++1	* CONSTANT *
71206	0760	00	0	00162		SWT	2	
71207	0420	00	0	00000		HPR		INDICATE ERROR
71210	0760	00	0	00161		SWT	1	
71211	0020	00	0	71213		TRA	++2	PROCEED
71212	0020	00	0	71200		TRA	OL6C	REPEAT THIS TEST

SECTION SEVEN

9M71B
2/1/63
PAGE 983

TEST THAT STR IN ODD LOCATION IS NOT DECODED AS TXL

71243	0500	00	0	71250	OL6F	CLA	OL6F1	SET UP
71244	0601	00	0	00002		STO	2	LOCATION TWO
71245	0774	00	1	00000		AXT	,1	
71246	0500	00	0	00002		CLA	2	***
71247	-1	77777	1	71251		STR	**2,1,-1	***
71250	0021	00	0	71253	OL6F1	TTR	**3	* CONSTANT *
71251	0760	00	0	00162		SWT	2	
71252	0420	00	0	00000		HPR		INDICATE ERROR
71253	0760	00	0	00161		SWT	1	
71254	0020	00	0	71256		TRA	**2	PROCEED
71255	0020	00	0	71243		TRA	OL6F	REPEAT THIS TEST

TEST THAT STR IN ODD LOCATION IS NOT DECODED AS STL

71256	0500	00	0	71264	OL6G	CLA	OL6G1	SET UP
71257	0601	00	0	00002		STO	2	LOCATION TWO
71260	0600	00	0	74225		STZ	TEMP	
71261	0761	00	0	00000		NOP		
71262	0500	00	0	74225		CLA	TEMP	***
71263	-1	62500	0	74225		STR	TEMP,,25920	***
71264	0021	00	0	71265	OL6G1	TTR	**1	* CONSTANT *
71265	-0520	00	0	74225		NZT	TEMP	SHOULD NOT SKIP
71266	0020	00	0	71271		TRA	**3	
71267	0760	00	0	00162		SWT	2	
71270	0420	00	0	00000		HPR		INDICATE ERROR
71271	0760	00	0	00161		SWT	1	
71272	0020	00	0	71274		TRA	**2	PROCEED
71273	0020	00	0	71256		TRA	OL6G	REPEAT THIS TEST

SECTION SEVEN

9M71B
2/1/63
PAGE 985

TEST THAT STQ IN ODD LOCATION IS NOT DECODED AS STZ

71332	0774	00	1	00000	OL6L	AXT	,1	
71333	0761	00	0	00000		NOP		
71334	0560	00	0	74210		LDQ	ONES	***
71335	-0600	00	0	74225		STQ	TEMP	***
71336	0500	00	0	74225		CLA	TEMP	
71337	-0100	00	0	71342		TNZ	**+3	SHOULD TRANSFER
71340	0760	00	0	00162		SWT	2	
71341	0420	00	0	00000		HPR		INDICATE ERROR
71342	0760	00	0	00161		SWT	1	
71343	0020	00	0	71345		TRA	**+2	PROCEED
71344	0020	00	0	71332		TRA	OL6L	REPEAT THIS TEST

TEST THAT CAL IN ODD LOCATION IS NOT DECODED AS TNX

71345	0774	00	1	60000	OL6M	AXT	24576,1	
71346	0500	00	0	71346		CLA	*	***
71347	-0500	00	1	71355		CAL	**+6,1	***
71350	-3 57777	1		71355		TXL	**+5,1,24575	SHOULD NOT TRANSFER
71351	0774	00	1	00000		AXT	0,1	CLEAR XRA
71352	0500	00	0	71352		CLA	*	***
71353	-0500	00	1	71355		CAL	**+2,1	***
71354	0020	00	0	71357		TRA	**+3	
71355	0760	00	0	00162		SWT	2	
71356	0420	00	0	00000		HPR		INDICATE ERROR
71357	0760	00	0	00161		SWT	1	
71360	0020	00	0	71362		TRA	**+2	PROCEED
71361	0020	00	0	71345		TRA	OL6M	REPEAT THIS TEST

TEST THAT TXI IN ODD LOCATION IS NOT DECODED AS CLA

71362	0774	00	1	00000	OL6N	AXT	0,1	
71363	0761	00	0	00000		NOP		
71364	0500	00	0	74212		CLA	MASK	***
71365	1 50000	1		71366		TXI	**+1,1,20480	***
71366	0402	00	0	74212		SUB	MASK	
71367	0100	00	0	71372		TZE	**+3	SHOULD TRANSFER
71370	0760	00	0	00162		SWT	2	
71371	0420	00	0	00000		HPR		INDICATE ERROR
71372	0760	00	0	00161		SWT	1	
71373	0020	00	0	71375		TRA	**+2	PROCEED
71374	0020	00	0	71362		TRA	OL6N	REPEAT THIS TEST

SECTION SEVEN

9M71B
2/1/63
PAGE 987

TEST TXI WITH 4X IN EVEN LOCATION

71421	0774 00 4 00000	OL8	AXT	0,4	CLEAR XR
71422	0400 00 0 74200		ADD	ONE	
71423	1 77776 4 71425		TXI	**2,4,32766	
71424	0020 00 0 71426		TRA	**2	
71425	3 77775 4 71430		TXH	**3,4,32765	CHECK RESULT
71426	0760 00 0 00162		SWT	2	
71427	0420 00 0 00000		HPR		ERROR ADD - TXI
71430	0760 00 0 00161		SWT	1	
71431	0020 00 0 71433		TRA	**2	
71432	0020 00 0 71421		TRA	OL8	

TEST TXI WITH 3X IN EVEN LOCATION

71433	0500 00 0 74200	OL9	CLA	ONE	
71434	0774 00 4 00000		AXT	0,4	
71435	0761 00 0 00000		NOP		
71436	0361 00 0 74200		ACL	ONE	
71437	1 77776 4 71441		TXI	**2,4,32766	
71440	0020 00 0 71442		TRA	**2	
71441	3 77775 4 71444		TXH	**3,4,32765	
71442	0760 00 0 00162		SWT	2	
71443	0420 00 0 00000		HPR		ERROR ACL-TXI
71444	0760 00 0 00161		SWT	1	
71445	0020 00 0 71447		TRA	**2	
71446	0020 00 0 71433		TRA	OL9	

TEST TXI WITH 2X IN EVEN LOCATION

71447	0560 00 0 74200	OL10	LDQ	ONE	SET UP
71450	-0754 00 0 00000		PXD	0,0	
71451	0774 00 4 00000		AXT	0,4	
71452	0200 00 0 74201		MPY	TWO	
71453	1 77776 4 71455		TXI	**2,4,32766	
71454	0020 00 0 71456		TRA	**2	
71455	3 77775 4 71460		TXH	**3,4,32765	CHECK RESULT
71456	0760 00 0 00162		SWT	2	
71457	0420 00 0 00000		HPR		ERROR MPY - TXI
71460	0760 00 0 00161		SWT	1	
71461	0020 00 0 71463		TRA	**2	
71462	0020 00 0 71447		TRA	OL10	

SECTION SEVEN

9M71B
2/1/63
PAGE 989

TEST TXI WITH AND TGR ON.

71513	0761	00	0	00000		NOP	
71514	0500	00	0	74210	OL13	CLA	ONES
71515	0774	00	4	00000		AXT	0,4
71516	-0320	00	0	74224		ANA	ZERO
71517	1	77776	4	71521		TXI	++2,4,32766
71520	0020	00	0	71522		TRA	++2
71521	3	77775	4	71524		TXH	++3,4,32765
71522	0601	00	0	74225		STO	TEMP
71523	-0520	00	0	74225		NZT	TEMP
71524	0020	00	0	71527		TRA	++3
71525	0760	00	0	00162		SWT	2
71526	0420	00	0	00000		HPR	
71527	0760	00	0	00161		SWT	1
71530	0020	00	0	71532		TRA	++2
71531	0020	00	0	71514		TRA	OL13

ERROR ANA - TXI

TEST TLA IN TRAP MODE

71532	0500	00	0	74227	OL14	CLA	TTR	STORE TTR RETURN FROM
71533	0774	00	4	00000		AXT	0,4	
71534	0601	00	0	00001		STO	1	
71535	0760	00	0	00007		ETM		
71536	0500	00	0	74210		CLA	ONES	
71537	1	77777	4	71543		TXI	++4,4,32767	SHOULD TRAP TO 1
71540	-0760	00	0	00007		LTM		PROPER RETURN FROM
71541	0020	00	0	71546		TRA	++5	TRAP
71542	0021	00	0	71544		TTR	++2	
71543	0021	00	0	71544		TTR	++1	
71544	-0760	00	0	00007		LTM		
71545	0020	00	0	71547		TRA	++2	
71546	3	00000	4	71551		TXH	++3,4,0	
71547	0760	00	0	00162		SWT	2	
71550	0420	00	0	00000		HPR		ERROR TLA-TXI IN TRAP
71551	0760	00	0	00161		SWT	1	MODE
71552	0020	00	0	71556		TRA	OL15	
71553	0020	00	0	71532		TRA	OL14	
71554	0535	00	2	00000	RET	LAC	0,2	RETURN FROM LOC 1
71555	0021	00	2	00001		TTR	1,2	